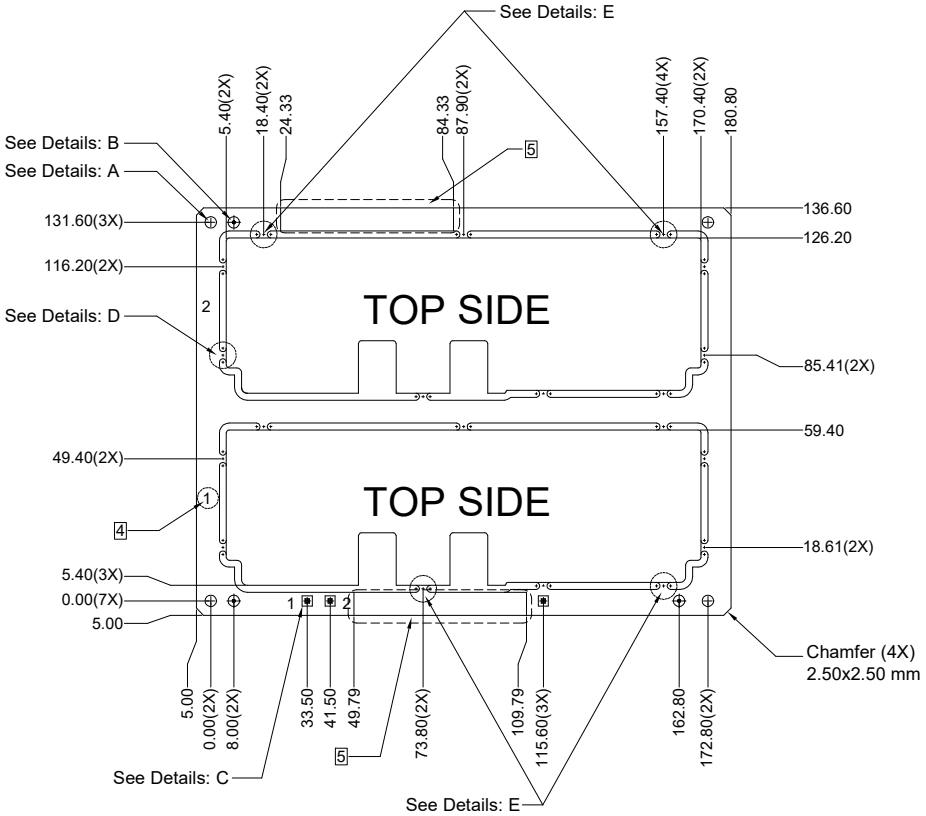
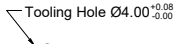
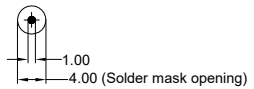
REVISIONS		
REV.	DESCRIPTION	DATE
E0	ENGINEERING EVALUATION RELEASE	10 NOV'2021

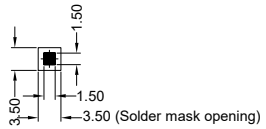




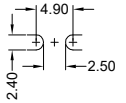
DETAILS : A (4 Places)



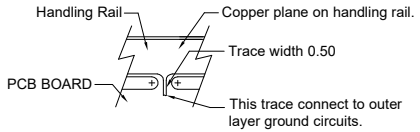
DETAILS: B (Top 3 Places)
(Fiducial)



DETAILS: C (Top 3 Places)
(Image Reject Marks)



DETAILS: D (20 Places)



DETAILS: E (Top 4 Places, Bottom 4 Places)

UNLESS OTHERWISE SPECIFIED

1. REFER TO GERBER PCB # R3059-B0002-03 REV.01
   (FILE NAME: R3059-B0002-03_R01_ODB_20211109.tgz)
   FOR INDIVIDUAL DRAWING OF SINGLE BOARD.
2. THIS DRAWING IS SHOWN THE CONFIGURATION
   OF PCB PANEL ONLY.
3. TOOLING HOLE ARE NON-PLATE.
4. BOARD NUMBER MADE ON SILKSCREEN LAYER TOP
   SPECIFICATION OF BOARD NUMBER REFER TO FAB DRAWING.
5. SOLDERMASK OPENING TOP AND BOTTOM LAYER(COVERED WITH SAME METAL FINISHING AS MAIN PCBs).
   SIZE TO BE 60.00mmX8.00mm OR THE ENTIRE WIDTH OF RAIL,EXTENDED TO OUTER PANEL EDGE.

"PRINTED CIRCUIT BOARDS SHALL BE ROHS COMPLIANT AND SHALL BE CAPABLE OF WITHSTANDING A PB-FREE ASSEMBLY ENVIRONMENT"

DESIGNED
APICHET C.
APPROVED
APIRAK R.

CONTROLLED DRAWING

GENERAL TOLERANCE
.XX ± 0.254
.XXX ± 0.127

ANGULAR ±3°
ALL DIMENSIONS
ARE IN MM.

DO NOT SCALE DRAWING

Celestica
49/18 Laemchabang Industrial Estate
Moo 5 Sriracha Chonburi Thailand
20230

TITLE :

ASSEMBLY PANEL OF JDM
FRONT IO CARD BOARD
2-UP/PANEL

FILE NAME : R3059-B0002-03-PN-E0.DWG
DRAWN BY: SOMSAK P.

DWG.NO. R3059-B0002-03-PN
REV. E0